

Substitute for Form 1449A/PTO (Modified) (Use as many sheets as necessary) Page 3 of 3				Attorney Docket No.: 42390P11711		Application Number: 09/960,543	
<div style="position: relative; height: 100px;"> <div style="position: absolute; top: 0; left: 0; width: 100%; height: 100%; border: 1px solid black; border-radius: 50%; text-align: center; line-height: 100px;"> OIP JAN 22 2002 TRADEMARK OFFICE </div> </div>				First Named Inventor: Deep K. Buch		RECEIVED FFR 0.5 2002 Group 2100	
				Filing Date: September 21, 2001			
U.S. PATENT DOCUMENTS							
Exam. Initial*	Cite No. ¹	U.S. Patent Document Number Kind Code ² (If known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
OTHER ART - NO PATENT LITERATURE DOCUMENTS							
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published				Translation ²	
GO		E. Armstrong, "Hot Spot: A New Breed of Virtual Machine", Java World (March 1998) http://www.javaworld.com/javaworld/jw-03-1998/jw-03-hotspot.html .					
GO		Dimpsey et al. "Java Server Performance: A case study of building efficient, scalable JVM's", IBM Systems Journal, Vol. 39, No. 1, 2000. http://www.research.ibm.com/journal/sj39-1.html .					
GO		Java HotSpot whitepaper, "Performance Engine Architecture", April 1999, http://java.sun.com/products/hotspot/whitepaper.html .					
GO		G.L. Peterson, "Myths About the Mutual Exclusion Problem", Information Processing Letters, Vol. 12 No. 3: Pages 115-116, June 13, 1981.					
GO		M. Ahamad et al. "The Power of Processor Consistency", In. Proc. 5 th Intl Symp on Parallel Algorithms and Architectures, pp. 251-260, June 1993. Tech Report GIT-CC-92/34, College of Computing, Georgia Institute of Technology. http://portal.acm.org/citation.cfm?id=165264&coll=portal&dl=ACM&CFID=1099009&CF...					
GO		L. Higham and J. Kawash, "Bounds for Mutual Exclusion with only Processor Consistency", Dept. of Computer Science, Univ. of Calgary, Canada. 14 Pages.					
Examiner Signature		GEORGE L. OPIE			Date Considered		9/29/04

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